AUTOMOTIVE CURRENT TRANSDUCER
HC6F700-S

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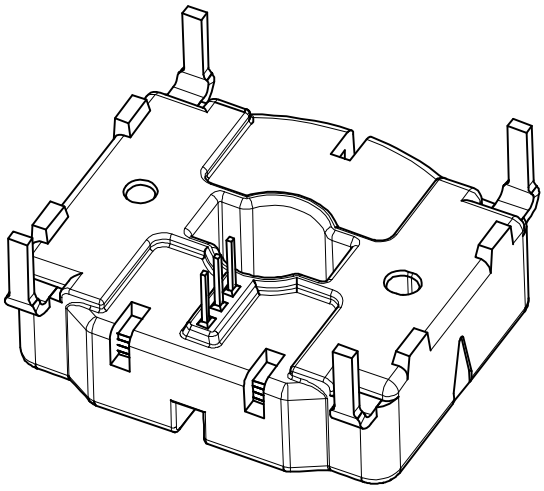

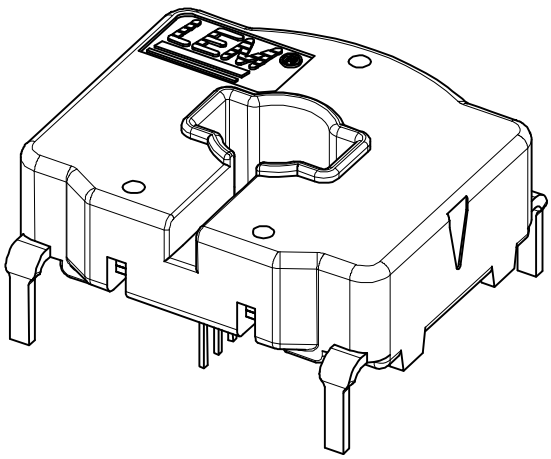

HC6F700-S

Introduction

The HC6F Family is for use on the electronic measurement of DC, AC or pulsed currents in high power and low voltage automotive applications with a galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).

The HC6F family gives you the choice of having different current measuring ranges in the same housing.

Features

- Open Loop transducer using the Hall effect
- Low voltage application
- Unipolar + 5 V DC power supply
- Primary current measuring range from 200 A up to 800 A
- Maximum rms primary admissible current: defined by busbar the magnetic core or the ASIC to have T° < + 150°C
- Operating temperature range: - 40°C < T° < + 125°C
- Output voltage: full ratio-metric (in gain and offset)
- Compact design for PCB mounting.

Advantages

- Excellent accuracy
- Very good linearity
- Very low thermal offset drift
- Very low thermal gain drift
- Wide frequency bandwidth
- No insertion losses
- Very good ratio size/current range.

Automotive applications

- Starter Generators
- Converters
- Inverters
- Drives.

Principle of HC6F Family

The open loop transducers use an Hall effect integrated circuit. The magnetic flux density B, contributing to the rise of the Hall voltage, is generated by the primary current $\mathbf{I}_P$ to be measured. The current to be measured $\mathbf{I}_P$ is supplied by a current source i.e. battery or generator (Fig. 1).
Within the linear region of the hysteresis cycle, B is proportional to:

$$B\ (\mathbf{I}_P) = \text{constant (a)} \times \mathbf{I}_P$$

The Hall voltage is thus expressed by:

$$\mathbf{V}_H = (\mathbf{R}_H/d) \times \mathbf{I}_b \times \text{constant (a)} \times \mathbf{I}_P$$

Except for $\mathbf{I}_P$, all terms of this equation are constant.
Therefore:

$$\mathbf{V}_H = \text{constant (b)} \times \mathbf{I}_P$$

The measurement signal V_H amplified to supply the user output voltage or current.

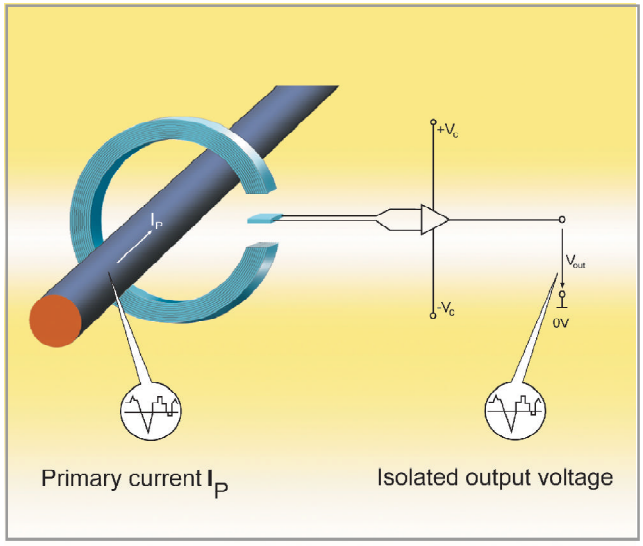

Fig. 1 : Principle of the open loop transducer

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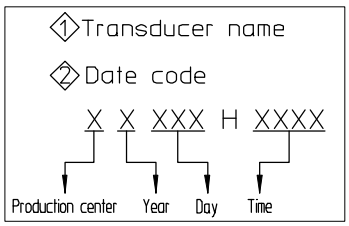
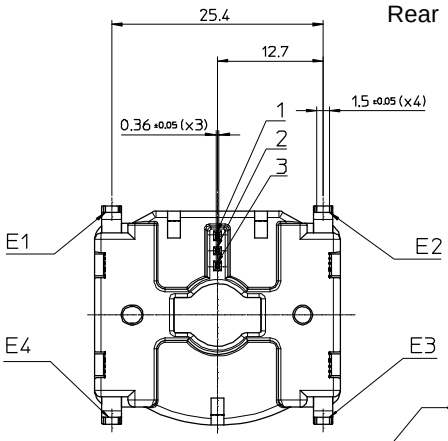
Dimensions HC6Fxxx-S family (in mm. 1mm = 0.0394 inch)

Secondary connection

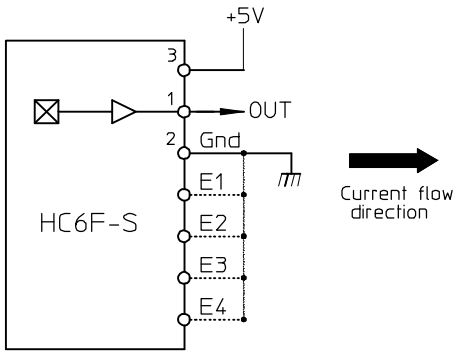
Terminals	Designations
3	Supply voltage + 5 V DC
1	V_{OUT}
2	Ground
E1 to E4	Ground (*)

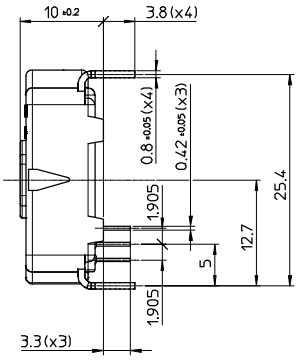
(*) Only 1 of these 4 pins could be connected

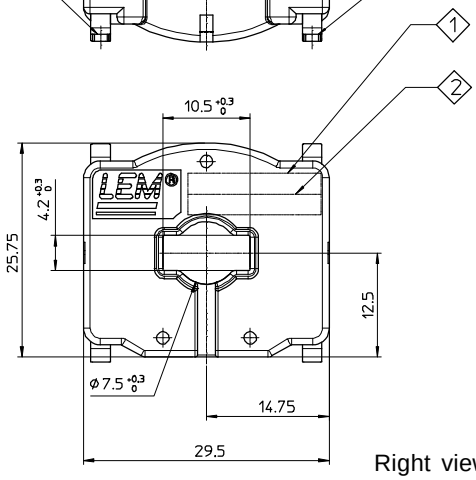




Connection







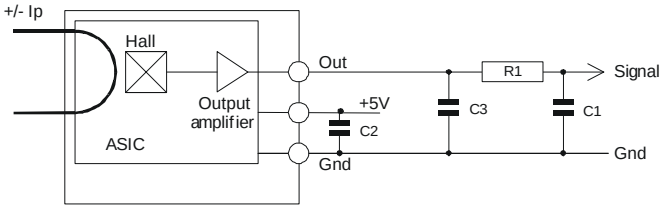
Bill of materials

• Plastic case	SPS GF 30
• Magnetic core	FeSi alloy
• Pins	Copper alloy tin platted (lead free)
Mass	23 g

Remarks

- General tolerance ± 0.2 mm
- $V_{OUT} > \frac{V_C}{2}$ when I_P flows in the direction of the arrow.

Electronic schematic



Power supply decoupling capacitor: C2 = 47 nF
EMC protection capacitor : C3 = 4.7 nF

Optional:
High frequency signal noise filter:
R1 > 100 Ω
C1 = defined according to the system frequency bandwidth.

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Absolute maximum ratings (not operating)

Parameter	Symbol	Unit	Specification	Conditions
Maximun peak primary current (not operating)	$I_{P\ max}$	A	Defined by busbar to have T° ≤ 150°C	
Primary nominal DC or current rms	I_{PN}	A	Defined by busbar to have T° ≤ 150°C	
Maximun supply voltage (not operating)	$V_{C\ max}$	V	7	
Secondary maximum admissible power	$P_{S\ max}$	mW	150	
Ambient operating temperature	T_A	°C	- 40 < T_A < 125°C	
Ambient storage temperature	T_S	°C	- 40 < T_S < 125°C	
Electrostatic discharge voltage	V_{ESD}	V	2000	see page 5/5
Maximum admissible vibration	γ	$m.s^{-2}$	100	see page 5/5
Rms voltage for AC isolation test 50 Hz, 1 min	V_d	V	2000	

Operating characteristics

	Symbol	Unit	Specification			Conditions
			Min	Typical	Max	
Electrical Data						
Primary current, measuring range	I_{PM}	A	-700	-	700	@ - 40°C < T° < 125°C
Supply voltage [1]	V_C	V	4.75	5	5.25	@ - 40°C < T° < 125°C
Output voltage (Analog)	V_{OUT}	V	$V_{OUT} = V_C/5 \times (2.5 + 0.0029 \times I_P)$			@ - 40°C < T° < 125°C
Sensitivity	**G**	V/A	0.0028	0.0029	0.0030	@ T_A = 25°C
Offset voltage	V_O	V	2.484	2.5	2.516	@ V_C = 5 V; T_A = 25°C; I_P = 0 A
Current consumption	I_C	mA	-	15	20	@ - 40°C < T° < 125°C; 4.75 V < V_C < 5.25 V
Load resistance	R_L	KΩ	2	-	-	
Output internal resistance	R_{OUT}	Ω	-	-	10	
Performance Data						
Sensitivity error	ε_G	%	-2.0	±0.7	2.0	@ T_A = 25°C, V_C = 5 V; Gth = 0.0029
Electrical offset	I_{OE}	A	-3.1	±1.4	3.1	@ V_C = 5 V; T_A = 25°C
	V_{OE}	mV	-9.0	±4.0	9.0	
Magnetic offset	I_{OM}	A	-2.4	±1.4	2.4	@ After excursion to ± I_P; T_A = 25°C
	V_{OM}	mV	-7.0	±4.0	7.0	
Temperature coefficient of	TCI_{OE}	mA/°C	-55.2	±27.6	55.2	@ - 40°C < T° < 125°C; V_C = 5 V
	TCV_{OE}	mV/°C	-0.16	±0.08	0.16	
Temperature coefficient of **G**	**TCG**	%/°C	-0.05	±0.02	0.05	@ - 40°C < T° < 125°C; V_C = 5 V
Linearity error	ε_L	% I_P	-1.0	±0.4	1.0	@ I_P; V_C = 5 V, T_A = 25°C
Response time	t_r	µs	-	8	15	@ di/dt = 50 A/µs; I_T = 400 A
Frequency bandwidth [2]	**BW**	kHz	20	-	-	@ -3 dB; I_T = 100 A rms
Output voltage noise peak-peak	$V_{nO\ p\text{-}p}$	mV	-	11.5	15.5	@ T_A = 25°C; 0 Hz < f < 1 MHz
Output voltage noise rms	$V_{nO\ rms}$	mV	-	2.5	4.0	@ T_A = 25°C; 0 Hz < f < 1 MHz

Notes: [1] The output voltage V_{OUT} is fully ratio-metric and depends on the supply voltage V_C.
The V_C value must be measured and used with the following formula:

$$I_P = \left(V_{OUT} - \frac{V_C}{2} \right) \times \frac{1}{G} \times \frac{5}{V_C} \qquad \textit{with G in (V/A)}$$

[2] Small signal only to avoid excessives heatings of the busbar, the magnetic core and the ASIC.

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PERFORMANCE PARAMETERS DEFINITIONS

Sensitivity:

The Transducer's sensitivity **G** is the slope of the straight line $\mathbf{V}_{OUT} = f(\mathbf{I}_P)$, it must establish the relation:
$\mathbf{V}_{OUT}(\mathbf{I}_P) = \mathbf{V}_C/5\ (\mathbf{G} \times \mathbf{I}_P + 2.5)$ (*)

(*) For all symetrics transducers.

Offset voltage:

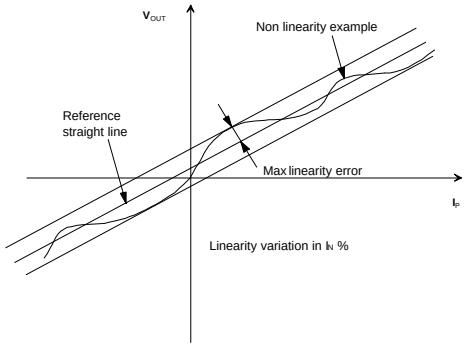
Is the output voltage when the primary current is null. The ideal value of $\mathbf{V}_O$ is $\mathbf{V}_C/2$. So, the difference of $\mathbf{V}_O - \mathbf{V}_C/2$ is called the total offset voltage error. This offset error can be attributed to the electrical offset (due to the resolution of the ASIC quiescent voltage trimming), the magnetic offset, the thermal drift and the thermal hysteresis.

Magnetic offset:

The magnetic offset is the consequence of an over-current on the primary side. It's defined after an excursion of $\mathbf{I}_P$ max.

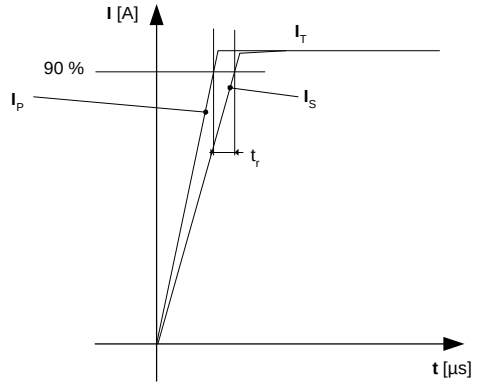
Linearity:

Is the maximum positive or negative discrepancy with a reference straight line $\mathbf{V}_{OUT} = f(\mathbf{I}_P)$.
Unit: linearity (%) expressed with full scale of $\mathbf{I}_P$ max.



Response time (delay time) t_r:

Is the time between the primary current signal and the output signal reach at 90 % of its final value.



Output noise voltage:

The output voltage noise is the result of the noise floor of the Hall elements and the linear $\mathbf{I}_C$ amplifier sensitivity.

Offset drift:

The error of the offset in the operating temperature εOffset is the relative variation of the offset in the temperature considered with the initial offset at 25°C. The offset temperature coefficient $\mathbf{TCV}_{OE}$ ($\mathbf{TCI}_{OE}$) in the operating temperature is the slope off εOffset = **f** (T).

Sensitivity drift:

The error of the sensitivity in the operating temperature Sensitivity Error is the relative variation of the sensitivity in the temperature considered with the initial sensitivity at 25°C. Sensitivity temperature coefficient **TCG**.

Typical:

Theorical value or usual accuracy recorded during the production.

Environmental test specifications

Name	Standard	Conditions
Thermal shocks	IEC 60068 Part 2-14	T° - 40°C to 125°C /1000 cycles not connected
Low T°operation at min supply voltage	IEC 60068 Part 2-1	T° - 40°C / 1000 H supply voltage = 4.75 V
High T° operation at max supply voltage	IEC 60068 Part 2-2	T° 125°C / 1000 H supply voltage = 5.25 V
Temperature humidity bias	IEC 60068 Part 2-3	T° 90°C / 95 % RH/ 1000 H supply voltage = 5.25 V
Mechanical Tests		
Vibration	IEC 60068 Part 2-64	Room T°, acceleration 100 m/s2, frequency 20 to 500 Hz/96 H each axis
Drop test	IEC 60068 Part 2-29	Height 750 mm concrete floor each directions
EMC Test		
Electrostatic discharge	JESD22-A114-B	Applied voltage = ± 2 kV pin to pin number of discharge = 1